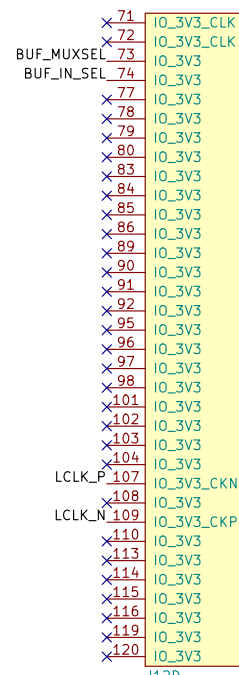
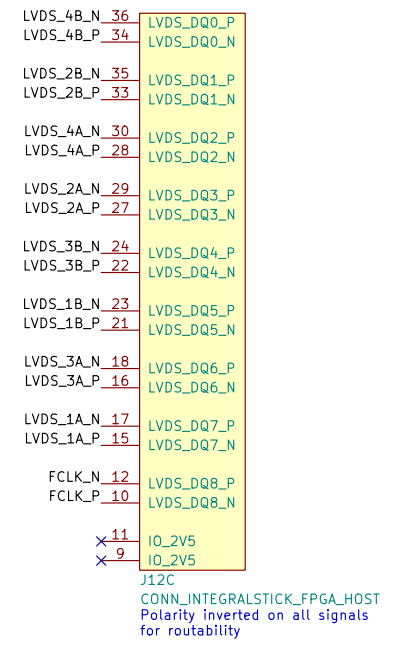
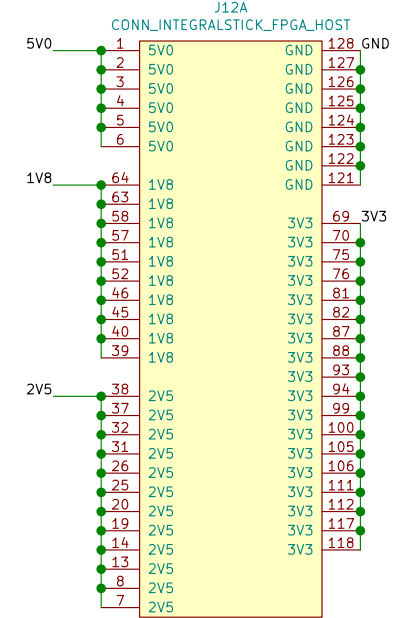


J12B
CONN_INTEGRALSTICK_FPGA_HOST
Can freely reassign



J12C
CONN_INTEGRALSTICK_FPGA_HOST
Can freely reassign except for LCLK



J12C
CONN_INTEGRALSTICK_FPGA_HOST
Polarity inverted on all signals
for routability